



**COPY OF PAPERS
ORIGINALLY FILED**

Sheet 1 of 1

Form PTO-1449

**U.S. Department of Commerce
Patent and Trademark Office**

Atty. Docket No.
7217/65957

Serial No.
10/052,984

**LIST OF PRIOR ART CITED BY APPLICANT
(Use several sheets if necessary)**

Applicant
Satoru Kawakami

Filing Date
11/07/01 Group

U.S. PATENT DOCUMENTS

Examiner Initial		Document Number							Date	Name	Class	Subclass	Filing Date if Appropriate
<i>PMW</i>	AA	5	4	8	3	4	1	3	01/09/96	Babb	361	220	10/24/94
	AB												
	AC												
	AD												

FOREIGN PATENT DOCUMENTS

		Document Number							Date	Country	Class	Subclass	Translation	
		AE	1	9	6	4	9	4		German Patent	H05K	9/00	Yes	No
<i>PMW</i>	AF	1	9	6	3	0	7	20	11/28/96	German Patent	H05K	1/16		X
<i>PMW</i>	AG	0	9	3	2	2	5	2	01/21/99	European Patent	H03D	7/16	X	
	AH													
	AI													

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

<i>PMW</i>	AL	Armstrong, M.K, PCB design techniques for lowest-cost EMC compliance: Part 1, <i>Electronics & Communication Engineering Journal</i> , August 1999, pp.185-194
	AM	
	AN	RECEIVED
		RECEIVED
	AO	SEP 20 2002
		TECHNology Center 2600
	AP	SEP 12 2002
		TECH CENTER 1600/2900
	AQ	
EXAMINER	<i>[Signature]</i>	DATE CONSIDERED
		<i>9/10/04</i>

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609: Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Best Available Copy